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Applicant: MATSUOKA, Hideyuki et al.
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Filed: 8 June 2000
For: SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD
THEREOF

Attached: Transmittal Letter to the U.S. Designation/Elected
Office, Xlation of Application and cover sheet of published
application, copy of Intl. Search Report, Preliminary Amendment,
copy of Form PCT/IB/308, 26 sheets formal drawings, and
copy of Intl. Preliminary Examination Report, and Check
#7752 (\$1,664)

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